Capacitance Sensitivity Calculation for Interconnects by Adjoint Field Technique

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Abstract

This paper presents a new, efficient algorithm for capacitance sensitivity calculation w.r.t. geometric variations due to process imperfection of interconnects. Sensitivity calculation can be a very important step in variation-aware interconnect analysis. The algorithm is based on the Adjoint Field Technique (AFT) derived from an application of Tellegen's theorem for the electrostatic (ES) field. The algorithm relies on manipulating the intermediate data of a standard (without considering variations) capacitance extraction. Thus no additional costly computations are required, which makes the algorithm very efficient. The algorithm has been verified for 2D structures while the generalization for 3D structures is straightforward.

Introduction

The on-going reduction of feature size goes together with an increase of manufacturing variability, of which the impact on the interconnect performance has become extremely important. In particular, capacitance extraction considering process variations has been gaining more and more attention. A popular approach is to use sensitivity models with the final capacitance as a function of geometry variations [1] [2] and [3].

A key requirement for this type of approach is the ability to calculate the sensitivity coefficients accurately and efficiently. Different techniques have been utilized to do that: numerical differencing in [2] and analytic differencing of empirical capacitance models in [1] [3]. In this paper, we propose a new technique of capacitance sensitivity calculation by the Adjoint Field Technique (AFT) which is in fact an application of Tellegen’s theorem [4].

The remainder of this paper is organized as follows: We start with an introduction of AFT for the ES field derived from Tellegen’s theorem; then a detailed analysis of our algorithm is given, followed by an illustrative example. Next, experiments and results will be shown to verify the algorithm and the conclusion is given at the end.

Adjoint Field Technique/Tellegen’s Theorem

Consider a circuit network with $m$ ports and $n$ internal branches (excluding the port branches). A combination of Kirchhoff’s current law and voltage law applied to the network can lead to

$$\mathbf{I}_m^T \mathbf{V}_m = \mathbf{I}_n^T \mathbf{V}_n$$  \hspace{1cm} (1)

while $\mathbf{I}_m$ and $\mathbf{V}_m$ are currents and voltages at the ports

The two networks are usually referred to as the original network and the adjoint/auxiliary network. To distinguish them, we use a notation “\( \hat{\cdot} \)” for the adjoint network quantities throughout this paper.

Originated from circuit networks, Tellegen’s theorem has been generalized to the electromagnetic fields [6] [7]. In this paper we will show another application of Tellegen’s theorem for the ES field.

Assume that there are $N$ conductors in the uncharged open space where the absolute zero potential is considered at infinity. We know the ES energy is

$$U = \frac{1}{2} \sum_{i=1}^{N} V_i q_i$$  \hspace{1cm} (3)

where $V_i$ is the absolute ES potential and $q_i$ is the total charge of the $i^{th}$ conductor ($i = 1, \ldots, N$) [8].

We also know that in the ES field, the stored electric energy for a configuration of static charges is the amount of work required to assemble such a configuration, which is given as [8] [9]

$$W = \frac{1}{2} \oint \mathbf{E} \mathbf{D} d\Omega.$$  \hspace{1cm} (4)

Apparently (3) and (4) are two different forms of the expression of the ES energy, which means $U = W$. This can be written as

$$\langle \mathbf{V}, \mathbf{q} \rangle = \langle \mathbf{E}, \mathbf{D} \rangle$$  \hspace{1cm} (5)

where $\langle \mathbf{V}, \mathbf{q} \rangle = \mathbf{V}^T \cdot \mathbf{q}$ and $\langle \mathbf{E}, \mathbf{D} \rangle = \int_{\Omega} \mathbf{E} \mathbf{D} d\Omega$. Equation (5) shows the energy conservation for the ES field and analogically to (1) and (2), we can, without proof, obtain Tellegen’s theorem for the ES field expressed as

$$\langle \hat{\mathbf{V}}, \hat{\mathbf{q}} \rangle = \langle \hat{\mathbf{E}}, \hat{\mathbf{D}} \rangle$$  \hspace{1cm} (6)

or

$$\langle \mathbf{V}, \mathbf{q} \rangle = \langle \hat{\mathbf{E}}, \hat{\mathbf{D}} \rangle.$$  \hspace{1cm} (7)

If $\mathbf{q}$ in the original network is changed by $\Delta \mathbf{q}$, based on

This work is supported by the European project CHAMELEON RF (IST-027378).
the linearity of the inner product, (6) can lead us to
\[
\langle \hat{V}, \Delta q \rangle = < \hat{\mathbf{E}}, \Delta \mathbf{D} > .
\]  
Similarly, we can derive from (7) that
\[
\langle \Delta V, \hat{q} \rangle = < \Delta \mathbf{E}, \hat{\mathbf{D}} > .
\]  
Subtracting (8) from (9) gives us
\[
(\Delta V, \hat{q}) - (\hat{V}, \Delta q) = < \Delta \mathbf{E}, \hat{\mathbf{D}} > - < \hat{\mathbf{E}}, \Delta \mathbf{D} > .
\]
\[\text{(10)}\]
It is well-known that \( q = \mathbf{C} \mathbf{V} \). Considering the fact that \( \Delta \mathbf{V} = 0 \) in our sensitivity study because the excitation voltages are not influenced by geometric variations, the change in \( q \) due to the variation in \( p \) can be written as
\[
\Delta q = (\Delta \mathbf{C}) \mathbf{V} + \mathbf{C} (\Delta \mathbf{V}) = (\Delta \mathbf{C}) \mathbf{V}
\]
\[\text{(11)}\]
In linear isotropic media we have \( \mathbf{D} = \epsilon \mathbf{E} \) and \( \hat{\mathbf{D}} = \epsilon \hat{\mathbf{E}} \). Thus using \( \Delta \mathbf{V} = 0 \), the substitution of (11) in (10) results in
\[
-(\hat{V}, (\Delta \mathbf{C}) \mathbf{V}) = < \Delta \mathbf{E}, \epsilon \hat{\mathbf{E}} > - < \hat{\mathbf{E}}, \epsilon \Delta \mathbf{E} > = < \Delta \mathbf{E}, \epsilon \hat{\mathbf{E}} > - < \epsilon \hat{\mathbf{E}}, \epsilon \Delta \mathbf{E} > = - < \hat{\mathbf{E}}, \epsilon \Delta \mathbf{E} > .
\]
Consequently,
\[
(\hat{V}, (\Delta \mathbf{C}) \mathbf{V}) = < (\Delta \epsilon) \mathbf{E}, \hat{\mathbf{E}} >
\]
\[\text{(12)}\]
This is the main result of AFT for our capacitance sensitivity calculation from a theoretical point of view. Next we will show how to develop (12) into an applicable algorithm.

Algorithm Analysis
To compute the sensitivity of a particular capacitance w.r.t. a certain geometric parameter, e.g., \( \frac{\partial C_{ij}}{\partial p_i} \), we should first select the specific capacitance \( C_{ij} \). For this purpose, let’s look at the left-hand side of Equation (12): \( (\hat{V}, (\Delta \mathbf{C}) \mathbf{V}) \). Assume that there are \( N \) conductors in the system and we have
\[
(\hat{V}, (\Delta \mathbf{C}) \mathbf{V}) = \begin{pmatrix} \hat{V}_1 \\ \vdots \\ \hat{V}_N \end{pmatrix}^T \begin{pmatrix} \Delta C_{11} & \cdots & \Delta C_{1j} & \cdots & \Delta C_{1N} \\ \vdots & \ddots & \vdots & \ddots & \vdots \\ \Delta C_{ij} & \cdots & \Delta C_{ij} & \cdots & \Delta C_{iN} \\ \vdots & \ddots & \vdots & \ddots & \vdots \\ \Delta C_{N1} & \cdots & \Delta C_{Nj} & \cdots & \Delta C_{NN} \end{pmatrix} \begin{pmatrix} V_1 \\ \vdots \\ V_j \\ \vdots \\ V_N \end{pmatrix}
\]
\[\text{(13)}\]
If we want to calculate \( \Delta C_{ij} \), we need to define the excitation voltages of the original system as
\[
V_k = 0 \quad \text{and} \quad V_0 = 1 \quad \text{for} \quad k \neq j;
\]
and the excitation voltages of the adjoint system as
\[
\hat{V}_k = 0 \quad \text{and} \quad \hat{V}_0 = 1 \quad \text{for} \quad k \neq i.
\]
Therefore,
\[
(\hat{V}_i, (\Delta \mathbf{C}) \mathbf{V}_j) = (\Delta C_{ij}) V_o^2 = \Delta C_{ij}
\]
where \( V_j \) is a vector whose elements are under condition (13) and \( \mathbf{V}_i \) is a vector whose elements are under condition (14).

Now we consider a more general situation where each conductor is discretized so that the Boundary Element Method (BEM) can be applied. For simplicity reasons, we use the piecewise constant (PWC) shape function for the rest of this paper. Each conductor is uniformly discretized with \( n \) panels which leads to a total number of \( m = n \times N \) panels in series in the system. For example, conductor \( j \) has panels numbered from \( (j - 1)n + 1 \) to \( jn \). The discretization is identical for the adjoint system.

We then use incidence matrices \( \mathbf{A} \) (for the original system) and \( \hat{\mathbf{A}} \) (for the adjoint system) to relate the panels to the applied voltage (\( \phi \in \{1, 0\} \)), which further, as will be explained later, determine the specific capacitance to be calculated.

For example, we want to calculate \( \Delta C_{ij} \). Based on (13), (14) and (15), \( 1(V) \) is applied to the \( j \)th conductor and \( 0(V) \) to the others for the original system while for the adjoint system \( 1(V) \) to the \( i \)th conductor and \( 0(V) \) to the others. Considering there are \( n \) panels on each conductor and making use of the incidence matrices \( \mathbf{A} \) and \( \hat{\mathbf{A}} \), we arrive at a generalization of (13) and (14):

\[
\mathbf{V}_j = \mathbf{A}_j \phi
\]
\[
= \begin{pmatrix} \text{zeros}(1, (j - 1)n)^T & \text{ones}(1, (j - 1)n)^T \\ \text{zeros}(1, (N - j)n)^T & \text{ones}(1, (N - j)n)^T \end{pmatrix} \begin{pmatrix} 1 \\ 0 \end{pmatrix}
\]
\[\text{(16)}\]
\[
\hat{\mathbf{V}}_i = \hat{\mathbf{A}}_i \phi
\]
\[
= \begin{pmatrix} \text{zeros}(1, (i - 1)n)^T & \text{ones}(1, (i - 1)n)^T \\ \text{zeros}(1, (N - i)n)^T & \text{ones}(1, (N - i)n)^T \end{pmatrix} \begin{pmatrix} 1 \\ 0 \end{pmatrix}
\]
\[\text{(17)}\]
where we use the Matlab notation to describe \( \mathbf{A}_j \) and \( \hat{\mathbf{A}}_i \). Substituting (16) and (17) in (15), we obtain
\[
\Delta C_{ij} = (\hat{\mathbf{V}}_i, (\Delta \mathbf{C}) \mathbf{V}_j) = \mathbf{V}_i^T (\Delta \mathbf{C}) \mathbf{V}_j = \phi^T \mathbf{A}_i^T (\Delta \mathbf{c}) \mathbf{A}_j \phi
\]
where \( c \) is the so-called partial capacitance matrix (\( c \)-matrix) [10]. The elements of \( c \)-matrix are in fact the capacitances associated to the BEM discretization panels before their association to conductors. Note that there are in total \( m \) panels in the system, thus \( \Delta \mathbf{c} \) (the variation in partial capacitances due to \( \Delta p \)) is an \( m \times m \) matrix. The choice of incidence matrices \( \mathbf{A} \) and \( \hat{\mathbf{A}} \) for the original and the adjoint systems respectively depends on the particular capacitance \( (\Delta C_{ij}) \) we want to calculate.

Next, let’s look at the right-hand side of Equation (12): \( < (\Delta \epsilon) \mathbf{E}, \hat{\mathbf{E}} > \), where \( \Delta \epsilon \) is the variation in the permittivity due to the variation in \( p \) (\( \Delta p \)).

By combining (12) and (15), we can derive that
\[
\frac{\partial C_{ij}}{\partial p} = \int_\Omega \frac{\partial}{\partial p} (\epsilon \hat{\mathbf{E}} \mathbf{E} d\Omega)
\]
\[\text{(18)}\]
where by definition
\[
\frac{\partial C_{ij}}{\partial p} = \lim_{\Delta p \to 0} \frac{\Delta C_{ij}}{\Delta p}.
\]
There are two different cases depending on how the surface of the conductor varies:

1. The surface of the conductor expands outwards into the air. As we can see in Figure 1(a), the shadow area is where \( \epsilon \) is changed. Specifically, the \( \epsilon \) in the shadow area varies from \( \epsilon_0 \) (\( \epsilon_{\text{air}} = \epsilon_0 \) in the air) to 0 (\( \epsilon_{\text{metal}} \approx 0 \) in the metal) so \( \epsilon \) is changed by a value of

\[
\Delta \epsilon = 0 - \epsilon_0 = -\epsilon_0. \tag{19}
\]

2. The opposite situation is that the surface shrinks (Figure 1(b)) and the \( \epsilon \) in the shadow area varies from 0 (in the metal) to \( \epsilon_0 \) (in the air):

\[
\Delta \epsilon = \epsilon_0 - 0 = \epsilon_0. \tag{20}
\]

![Figure 1: Schematics for the changes of \( \epsilon \).](image)

Since the variation of the permittivity due to \( \Delta p \) only happens on the corresponding surface \( s_p \), which is moved due to \( \Delta p \), we have

\[
d\Omega = \Delta p ds
\]

where \( d\Omega \) is basically the shadow area in Figure 1(a)/(b). Therefore the sensitivity (18) can be rewritten as,

\[
\frac{\partial C_{ij}}{\partial p} = \int_{s_p} \lim_{\Delta p \to 0} \left( \frac{\Delta \epsilon}{\Delta p} \right) \mathbf{E} \cdot \mathbf{E} ds = \Delta \epsilon \int_{s_p} \mathbf{E} \cdot \mathbf{E} ds \tag{21}
\]

When the BEM is applied, \( \mathbf{E} \) is a piecewise constant quantity on the set of panels. Hence, the integration over \( s_p \) becomes a summation. When also using \( \mathbf{D} = \epsilon \mathbf{E} \), we get

\[
\frac{\partial C_{ij}}{\partial p} \epsilon \int_{s_p} \mathbf{E} \cdot \mathbf{E} ds = \Delta \epsilon \sum_{k \in s_p} E_k \hat{E}_k a_k = \frac{\Delta \epsilon}{\Delta \epsilon} \sum_{k \in s_p} D_k \hat{D}_k a_k
\]

where \( a_k \) is the corresponding area of the \( k^{th} \) panel on the conductor (\( s_p \)). With Gauss law \( \nabla \cdot \mathbf{D} = \rho \) (where \( \rho \) is the charge density) applied to the conductor surfaces, (22) can be written

\[
\frac{\partial C_{ij}}{\partial p} = \frac{1}{\Delta \epsilon} \sum_{k \in s_p} \rho_k \hat{D}_k a_k. \tag{23}
\]

For a panel \( k \ (k \in \{1, \cdots, m\}) \), since PWC is used for the BEM, the charge density \( \rho_k \) can be related to the charge \( q_k \) with the corresponding area \( a_k \) as \( q_k = \rho_k a_k \), which leads to

\[
\frac{\partial C_{ij}}{\partial p} = \frac{1}{\Delta \epsilon} \sum_{k \in s_p} q_k \hat{a}_k. \tag{24}
\]

Based on (16) we have

\[
q = CV_j = cA_j \phi
\]

\[
= \begin{pmatrix}
  c_{1, j_1} + c_{1, j_2} + \cdots + c_{1, j_n} \\
  c_{2, j_1} + c_{2, j_2} + \cdots + c_{2, j_n} \\
  \vdots \\
  c_{m, j_1} + c_{m, j_2} + \cdots + c_{m, j_n}
\end{pmatrix}
\]

\[
= \sum_{a=(j-1)n+1}^{jn} c_{a, a}
\]

Similarly, based on (17) we have

\[
\hat{q} = CV_j = c\hat{A}_j \phi
\]

\[
= \begin{pmatrix}
  \sum_{b=(i-1)n+1}^{in} c_{1, b} \\
  \sum_{b=(i-1)n+1}^{in} c_{2, b} \\
  \vdots \\
  \sum_{b=(i-1)n+1}^{in} c_{m, b}
\end{pmatrix}
\]

\[
= \sum_{b=(i-1)n+1}^{in} c_{k, b}
\]

The substitution of (25) and (27) in (24) gives us the final equation which will be used for the calculation of capacitance sensitivities:

\[
\frac{\partial C_{ij}}{\partial p} = \frac{1}{\Delta \epsilon} \sum_{k \in s_p} \left( \sum_{a=(j-1)n+1}^{jn} c_{k, a} \right) \left( \sum_{b=(i-1)n+1}^{in} c_{k, b} \right) / a_k. \tag{28}
\]

The development above actually presents the sensitivity in terms of partial capacitances, which can be extracted by the BEM-based layout-to-circuit extractor SPACE [11]. In the next section, we will use an example to illustrate the algorithm.

**Illustrative Example**

This is a system with two conductors (\( N = 2 \)) as shown in Figure 2(a). In order to make the explanation clear and easy to understand, we assume that there are 2 panels on every surface and therefore \( n = 8 \) panels on each conductor. The system has thus altogether \( m = n \times N = 16 \) panels. The geometry parameter \( p \) is the width of conductor 1 and the affected surface \( s_p \) consists of panels nr. 7 and 8.

According to (28), the sensitivity of the mutual capacitance \( C_{21} \) between conductor 1 and 2 w.r.t. \( p \) can be calculated as (Figure 2(b)),

\[
\frac{\partial C_{21}}{\partial p} = \frac{1}{\Delta \epsilon} \left[ \sum_{a=1}^{8} c_{7, a} \left( \sum_{b=9}^{16} c_{7, b} \right) a_7 + \sum_{a=1}^{8} c_{8, a} \left( \sum_{b=9}^{16} c_{8, b} \right) a_8 \right].
\]
Experiments and Results

In this section, two experiments will be shown to verify the accuracy of the algorithm described above. Again, we assume a system with two conductors for simplicity reasons (the schematic is the same as Figure 2(a)), but this is not a restriction of the method. In the first experiment, the distance between these two conductors \(d\) is taken as the geometric parameter. The widths of the conductors are \(2\)\(\mu m\) and the heights are \(3\)\(\mu m\). The total number of panels is 500. The main steps of the experiment are as follows:

1. Use the BEM to compute \(C\) as a function of \(d\), resulting in \(C_{BEM}(d)\).

2. Fit \(C_{BEM}(d)\) with appropriate polynomial approximations. In this experiment, \(1/d\) is taken as the variable of the polynomial and 2\(^n\) order expansion of \(C\) is utilized:

\[
C_{\text{approx.}}(d) = m_0 + m_1 \frac{1}{d} + m_2 \left(\frac{1}{d}\right)^2.
\]

To achieve a high accuracy, the approximation is further divided into several subsections (i.e., three subsections in this case).

3. Use the polynomial fit above to compute the sensitivity by calculating the derivatives of the approximated capacitance w.r.t. \(d\), resulting in \(\frac{\partial C}{\partial d}\) (where the notation \(\text{“n”}\) represents for the numerical differencing method), which is considered as the reference of the sensitivity computed by the AFT method \(\frac{\partial C - A}{\partial d}\).

4. Compute the sensitivity using (28), and compare the result \(\frac{\partial C - A}{\partial d}\) with \(\frac{\partial C}{\partial d}\).

Figure 3 shows \(\frac{\partial C}{\partial d}\) and \(\frac{\partial C - A}{\partial d}\) w.r.t. \(d\) in the range of \(0.5\mu m - 2\mu m\). The sensitivity calculated by AFT agrees well with \(\frac{\partial C}{\partial d}\), which is taken as the reference. Table 1 shows the average and the maximum differences between them while the distance ranges from \(0.5\mu m\) up to \(12\mu m\) divided into three subsections.

<table>
<thead>
<tr>
<th>Distance((\mu m))</th>
<th>Average Deviation</th>
<th>Max. Deviation</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.5 - 2</td>
<td>1.74%</td>
<td>2.99%</td>
</tr>
<tr>
<td>2 - 6</td>
<td>2.82%</td>
<td>4.76%</td>
</tr>
<tr>
<td>6 - 12</td>
<td>3.82%</td>
<td>4.93%</td>
</tr>
</tbody>
</table>

Table 1: Comparison between \(\frac{\partial C}{\partial d}\) and \(\frac{\partial C - A}{\partial d}\).

We also conducted another similar experiment with \(t\), the thickness of the conductors, instead of \(d\) as the geometric parameter. The system contains two conductors with a width \(w = 2\mu m\) and a separation distance \(d = 3\mu m\). The total number of panels is 2000. The average and the maximum differences between \(\frac{\partial C}{\partial t}\) and \(\frac{\partial C - A}{\partial t}\) are 5.36% and 5.74% respectively over the range of \(t = 2\mu m - 5\mu m\).

Conclusion and Future Work

A new AFT-based algorithm is proposed for the calculation of capacitance sensitivity w.r.t. geometric variations for capacitance extraction considering process variability. This highly efficient algorithm requires no additional costly computation once the partial c-matrix is extracted. Although so far the experiments shown above only consider 2D interconnect structures, we believe the algorithm can be applied to 3D structures as well.

References


